Amendments to the Specification:

Please replace paragraph [0037] of the published patent application no. 2004-0160402 with the following amended paragraph:

[0037] The sampling/holding unit 520 includes sixteen sampling/holding circuits for sampling the data from the D/A converter 830 of the gray voltage generator 800, which includes eight circuits for positive voltages and eight circuits for negative voltages. The sampled voltages are applied to nodes between the resistors 510 as shown in FIG. 4 such that they determine the finalized values of the gray voltages output from the nodes as indicated by the arrows. Each sampling/holding circuit includes a switch SW, a capacitor C1, and a buffer B_1-B_{16}. When a switch SW turns on in response to a sampling start signal, ~~reference~~ gamma reference voltages from the D/A converter are sampled and stored in the capacitors C1, and the sampled gamma reference voltages are output from the analog buffers B_1-B_{16}. The output voltages are divided into a plurality of gray voltages, for example, 256 positive analog gray voltages and 256 negative analog gray voltages (2^8=256).